

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** ( Not for submission under 37 CFR 1.99)

Application Number		10807094
Filing Date		2004-03-22
First Named Inventor	Dimpsey et al.	
Art Unit	2189	
Examiner Name	Flournoy, Horace L.	
Attorney Docket Number	AUS920040059US1	

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	2	6549959	B1	2003-04-13	Yates et al.	
	3	6772322	B1	2004-08-03	Merchant et al.	
	4	5937437		1999-08-10	Roth et al.	
	5	6070009		2000-05-30	Dean et al.	
	6	6381679	B1	2002-04-30	Matsubara et al.	
	7	6687794	B2	2004-02-03	Malik	
	8	6539458	B2	2003-03-25	Holmberg	

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9	6973542	B1	2005-12-06	Schmuck et al.	
10	6782454	B1	2004-08-24	Damron	
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1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
2	"Cache Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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- ☐ See attached certification statement.
- ☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Wayne P. Bailey/	Date (YYYY-MM-DD)	2006-11-09
Name/Print	Wayne P. Bailey	Registration Number	34289

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